

Bagilipeddola Chaitanya
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Experience Summary

IT Professional with 3.5 years of experience in effective management of Post silicon USB\TYPE-C Validation

Objectives:

Seeking a challenging environment that encourages continuous learning, creativity, exposure to new ideas and simulates personal and professional growth where I can apply and upgrade my knowledge.

Career Record:

- Harman Connected Services, Bangalore – Product Engineer (March 2022 to Current Date.)

Academic Education:

- BE IT Rishi MS institute of Engineering and Technology (2018-2022)
- MPC in Sri Shirdi Sai Mahila junior college, Bodhan (2016-2018)
- SSC in Sharada High School (2015-2016)

Career Summary:

- Understanding of PC Platform and motherboard architecture.
- Hands-on experience in USB/Type-C/TBT Storage devices & Docks.
- Good knowledge in Latest feature of USB4
- Test case execution coverage on TBT/Type-C-Domain
- Validating basic features of TYPE-C, Display & Audio
- Good understanding of different power states of system and devices (C-states)
- Expertise in system stability (Video Playback, Idle, Cold boot, Warm boot, Sleep test)
- Experience in validating Customer End products of Intel processors like DELL, HP AND LENOVA .
- Firmware updates like BIOS, EC, PD, RETIMER, DOCKS & USB4 using both hardware and software tools.
- Experience in FW flashing instruments: Dediprog-SF600 AND ADVARK (FLASH CENTER).
- Expertise in working in R/W Tool, Socwatch Tool AND USB-Tree view and USB view tool
- Having hands-on experience in debug tools like Ellisys (PD, I2C, MR, Retimer Logs)
- Triggering Test cases in Jenkins automation testing tool.

Core Competencies:

- Post silicon SoC-TYPE-C validation.
- Client Interaction & Coordination
- Test Strategy & Execution
- Software Testing Life Cycle, knowledge on Defect Life Cycle
- knowledge in USB4 Functionality

- Expertise in system stability(Video Playback, Idle, Cold boot, Warm boot, Sleep test)

Validation Activities

- I work closely with the development team to understand and gain knowledge of platforms.
- Performing sanity test on Windows platform and reporting the issue found on system power states like S0, S3, S4, S5.
- Currently working for the PD BA team for TCSS and have experience in triaging and doing BAT Testcases.
- Worked in Upcoming Processors/Boards (**Meter Lake**): Intel Microprocessor which they have introduced ifwi & BIOS.
- Preparing Test reports for executed testcases.
- Finding BUGS -Validate & Providing validation training to teammates.

Played a key role in:

- TCSS E2E validation activities and Board bring up, Various types of Intelplatforms.
- Worked in MTL_[S] platforms and currently working in NVL_[S]platform.

Area of Expertise:

- Test Strategy & Execution
- Hardware Validation
- Automation Testing

Skill Summary:

- I worked extensively on **SoC-USB/TYPE-C validation** on the windows platform.
- I have a very good knowledge of **Software Testing Life Cycle**.
- Well known of different types of **Functional Testing**.

Technical Skills:

Tools & OS	Tera-Term, Windows OS
Platforms	Windows
Bug Tracking Tools	HSD
Debug tools known	I2C, PD-Retimer, PD-EC, PD-PMC Log capturing.

Address:

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